Electronic Acknowledgement Receipt				
EFS ID:	1273929			
Application Number:	09902051			
International Application Number:				
Confirmation Number:	7744			
Title of Invention:	CMOS DC offset correction circuit with programmable high-pass transfer function			
First Named Inventor/Applicant Name:	Mark J. Chambers			
Customer Number:	23494			
Filer:	Carlton H. Hoel/Gracia Sansom			
Filer Authorized By:	Carlton H. Hoel			
Attorney Docket Number:	TI-30883			
Receipt Date:	25-OCT-2006			
Filing Date:	10-JUL-2001			
Time Stamp:	17:12:15			
Application Type:	Utility			

Payment information:

Submitted with Payment	no	
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part /.zip	Pages (if appl.)
1	Letter Express Abandonment of the application	TI-30883-ExpressAbandon.p df	7188	no	1
Warnings:					

Information:	
Total Files Size (in bytes):	7188

This Acknowledgement Receipt evidences receipt on the noted date by the USPTO of the indicated documents, characterized by the applicant, and including page counts, where applicable. It serves as evidence of receipt similar to a Post Card, as described in MPEP 503.

New Applications Under 35 U.S.C. 111

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.